1. Fill up the following table. Show necessary calculations.

|  |  |  |  |
| --- | --- | --- | --- |
| Decimal | Binary | Octal | Hexadecimal |
| 240.6  **Set B**  **Set A** | **Set A** | **Set A** | **Set A** |

|  |  |  |  |
| --- | --- | --- | --- |
| Decimal | Binary | Octal | Hexadecimal |
| 321.4 |  |  |  |

A'

B'

C

F4

**F4 = (A'B'+C)'**

Design the above logic circuit using only **NAND gate (Set-A) / NOR gate (Set-B)**